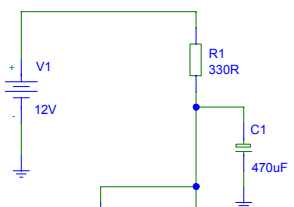
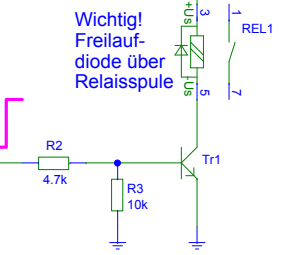
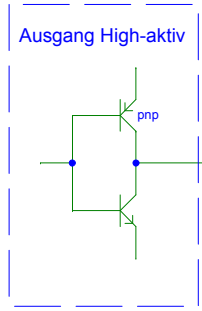


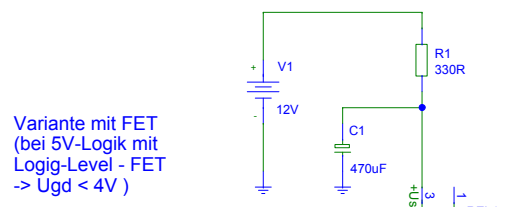
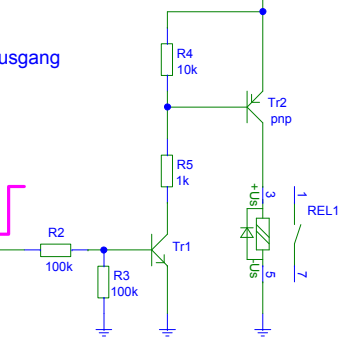
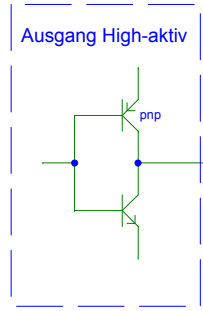
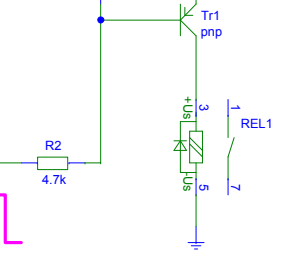
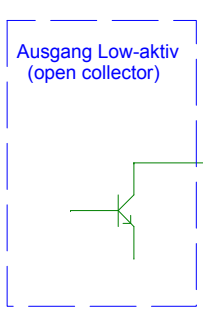
Stromsparschaltung mit R1 und C1so anpassen, dass das Relais sicher anzieht.



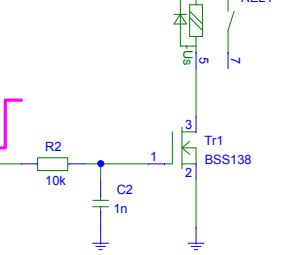
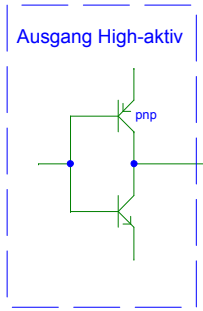
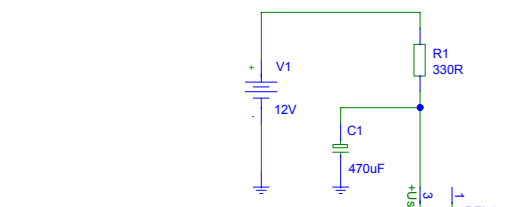
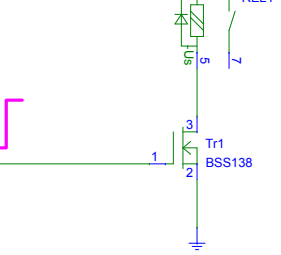
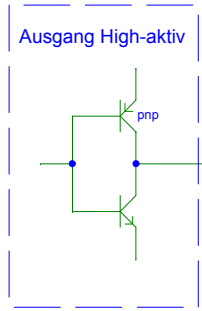
Variante zur Erhöhung des Treiberstromes bei Hochstromlasten bzw. sehr schwachem Ausgang



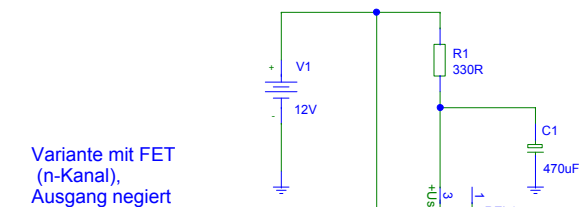
Wichtig! Freilaufdiode über Relaispule



Variante mit FET (bei 5V-Logik mit Logig-Level - FET -> Ugd < 4V)



R2 und C2 optional zur Unterdrückung von Störimpulsen.



Variante mit FET (n-Kanal), Ausgang negiert

